



Prof. A.K. Bisoi joined Utkal University in 1970 as a research scholar in the Dept of Physics. He served the University from 1971 to 2011 in various capacities, (Physics: Research Assistant, *Computer Science*: Asst Programmer, Programmer, Lecturer, Reader, Professor). During the period from 1972 to 1981, he conducted six-week courses on the FORTRAN programming language, 28 times using the newly installed IBM 1130 computer, in which about 1500 persons from various institutions were trained.

During 1976-1977 he was a **DAAD scholar** at the *Goethe Institute, Prien* and the *Johannes Gutenberg Universitat, Mainz*, Germany. In 1978 he obtained a Ph D in Particle Physics under the guidance of Prof B. B. Deo.

He joined as a Lecturer in Computer Science in 1984, when the Post-B. Sc. Diploma course in Computer Application was launched. Subsequently, he became a Reader in Computer Science in 1990 and a Professor in Computer Science in 1999. In the meanwhile, during 1996-2001 he had served at the **IGIT, Sarang** as Professor and Head in Computer Science, on lien from Utkal University. Post retirement, he has served as a Professor in the School of Computer Engineering at the **KIIT University** during 2011-2016

He has been an Associate member of **ICTP Trieste, Italy** in the field of Computational Physics and has visited ICTP, five times during 1986-1996.

Other scholarships awarded to him are: Science Talent Scholarship, Govt of Madhya Pradesh (1965-1968) and Post Graduate Scholarship, Dept of Atomic Energy (1969-1970).

His areas of research include: Theoretical Particle Physics, Computer Algebra, Fractals Chaos and Computer Graphics, Image Processing, Software Engineering. He has 15 Journal and 12 Conference publications along with 2 ICTP Reports, He has guided four Ph.D. students in Computer Science.

The various **administrative positions** occupied by him are: Head, Dept of CSA; Coordinator, Integrated MCA; Director, School of Mathematics, Statistics & Computer Science; Prof in Charge, Computer Centre; Utkal University. Head, Dept Computer Science, IGIT, Sarang; Chairman, MCAJEE subcommittee of Odisha (1998 &1999); Dean, School of Computer Engineering, KIIT University.



Late Dr. BIKRAM KESARI RATHA joined the department at Utkal University as Lecturer during 1991 and became Reader in 2012. Prior to this, he worked as a Lecturer at I.G.I.T, Saranga, from 17.07.89 to 31.12.89, TA at I.I.T., Khargpur from 01.01.90 to 04.03.90, Lecturer at I.G.I.T, Saranga, from 05.03.90 to 28.11.91, Professor at Dept. of CSE, NEPAL ENGINEERING, COLLEGE, Kathmandu, Nepal from 01.03.01 to 27.02.05(Pokhra University) and Professor & Head at School of Technology, (Lecture Sub Centre)

S.R.T.M., UNIVERSITY, Nanded MAHARASHTRA from 30.09.11 to 02.06.12. He Completed his B.Sc. (Mathematics Hons) from OUAT, Bhubaneswar in 1984, MCA from Sambalpur University (NIT Rourkela) in 1989, ME(CSE) Sambalpur University (NIT Rourkela) in 1998 and Ph. D. (CS) from Utkal University, Bhubaneswar in 2010.

His areas of interests include: Software Engineering, Sensor Networks, Data Mining, ICT Applications. He has published his research work in several international Journal and conferences of repute. He has guided 10 Ph.D. scholars and several PG students. Besides these, he has served the Utkal university and others in different capacities.



Dr. P K Meher is well-known for his outstanding research contribution in the field of VLSI chip design and computer applications. He has the unique experience of teaching and research in Physics, Electronics, and Computer Engineering for more than 35 years. He started his teaching career as a Lecturer in Physics in 1981 and worked in various government colleges in Odisha till 1993. Thereafter he served the Berhampur University, Berhampur, Odisha as a Reader in Electronics during 1993- 1997. He joined the Department of Computer Science & Applications in Utkal

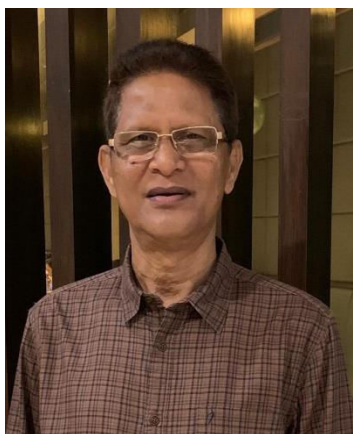
University as the first Professor in March 1997 and has served as Head of the Department during 1997-1999. He had started the Five Year Integrated MCA course in Utkal University as its first Co-ordinator in the year 1999.

He has demonstrated various models of collaborative research and provided expert research mentoring to several doctoral and post-doctoral candidates. Recently, he has co-edited a book, 'Arithmetic Circuits for DSP Applications' which is published by the IEEE Press. He was conferred with the Samanta Chandrasekhar Award for Research in Engineering & Technology, for the year 1999. Some of his other recognitions include the Best Paper Award of the Springer Journal of Circuits, Systems and Signal Processing for the year 2013 and the Sydney R. Parker Best Paper Award for the year 2016.

Dr. Meher has served the Editorial Board of various IEEE Transactions and the Springer Journal of Circuits, Systems, and Signal Processing. As a speaker of the Distinguished Lecturer Program of IEEE Circuits & Systems Society, he has delivered talks in India and several other countries. Dr. Meher is a Life Fellow of the Institution of Electronics and Telecommunication Engineers, India and a Senior Member of the Institute of Electrical and Electronics Engineers (IEEE) from 2003. He was elected as a Fellow of the Institution of Electrical Engineers (IEE), UK, in 2004. He was registered as a Chartered Engineer in the Engineering Council of UK in the year 2005.



Late Prof. Sanghamitra Mohanty (1 April 1953 – 1 July 2021) was an Indian computer scientist. She had a M.Sc. and Ph.D. in physics. She worked as a lecturer, reader and professor in computer science at Utkal University from 1986 to 2011. She did research in the fields of artificial intelligence, speech processing, image processing, natural language processing, fractal geometry, weather prediction, and high energy physics. Prof. Mohanty had thirteen Intellectual Property Rights (IPRs) on Indian Language Technology Solutions. More than 160 of her research papers were published in international journals and conference proceedings. She visited a number of institutions in India and abroad for academic collaboration and research. She was the recipient of the Samanta Chandrashekhara Award of Odisha Bigyan Academy of Science and Technology Department, Government of Odisha for her contribution to engineering and technology in 2012. She was a Fellow of the Women's Engineering Society of United Kingdom (FWES). Prof. Mohanty was the Vice Chancellor of North Orissa University during 2011 to 2014. She was the President of Odisha Bigyan Academy starting in January 2016.



Dr Sateesh Kumar Pradhan joined the Department as a Reader in Computer Science during October 2000 and become Professor in 2014. Prior to joining the Department, he served as Lecturer and Reader in Computer Science Berhampur University Odisha. Dr. Pradhan was a visiting faculty to King Khalid University, Abha. Kingdom of Saudi Arabia during the year 2006-2011. He completed M.Sc. from GM College Sambalpur University in 1983 and P.G.D (Computer Science) from North Bengal University, Darjeeling in 1985, and Ph.D. (Computer Science) From Berhampur University in 1999. He served as the Head of the Department from 2001 to 2003 and 2011 to 2016. Prof Pradhan was involved in the academic activities of Utkal and other Universities in the state in different capacities.

His research area is mainly Neural Computing, Parallel Processing, Optical Character Recognition of Odia Text and Wireless Sensor Network. Prof Pradhan has successfully guided 22 PhD students and published 40 papers in National and International Journals, and 60 papers in Conferences. A number of students are still working for PhD degree under his guidance. Professor Pradhan has authored as many as five text books (Odia Language) sponsored by government of Orissa. In the Department of Computer Science, Utkal University, he established two excellent laboratories for research scholars as well as for students.

Besides academic activities, he has served the University in various important administrative positions as Professor-in-Charge, Computer Centre, Dean, Faculty of Engineering, Course Coordinator, Integrated MCA course, Course Coordinator, 3 Years MCA, DDCE, Course Coordinator, M. Sc. (Computer Science).

He superannuated from active service in December, 2020.